In re application of: B. Davis et al.

Filed: December 28, 2000

Title: NUMA SYSTEM RESOURCE DESCRIPTORS INCLUDING
                        PERFORMANCE CHARACTERISTICS

Assistant Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Before calculation of the filing fee or examination, please amend the above-identified application as follows:

In the Specification

On page 8, at line 29: change "node" to —processor—, and change "64" to —140—.

In the Claims

Please amend the claims to read in full as follows:

1. (Amended) A computer system, comprising:

    multiple processors,

    a plurality of resources assigned to node groups;

    a first descriptor of respective topological levels of at least one of the resources; and

    a second descriptor of respective performance of said resources ._

    wherein the first and second descriptors are produced by firmware .

2. (Unchanged) The system of claim 1, wherein said first descriptor is a first level data structure, and said second descriptor is a primary data structure.